

**REPLY UNDER 37 CFR §1.116  
EXPEDITED PROCEDURE  
ART UNIT 1765**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Chung-Hui CHEN, et al.	§	Docket No.:	2001-1339 / 24061.420
		§		
Serial No.:	10/820,396	§	Examiner:	Anita K. Alanko
		§		
Filing Date:	April 8, 2004	§	Art Unit:	1765
		§		
For:	Deep Submicron CMOS	§	Conf. No.:	8456
	Compatible Suspending Inductor	§		
		§		

**AMENDMENT**

Commissioner for Patents  
Mail Stop: AF  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Madam:

No fees, including extension of time fees, are believed necessary for consideration of the present paper. However, if any fees, including extension of time fees, are necessary, the extension of time is hereby requested, and the Commissioner is hereby authorized to charge any fees, including those for the extension of time, to Haynes and Boone, LLP's Deposit Account No. 08-1394.

The present paper is being submitted in response to the Final Office Action dated October 19, 2006 in the above-identified application.

**Amendments to the Claims** are reflected in the **Listing of Claims** beginning on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.